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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

TSUGANE et al.

Serial No. 09/759,715

Filed: January 13, 2001

For: SEMICONDUCTOR DEVICES AND METHODS

FOR MANUFACTURING THE SAME

Group Art Unit: 2811

Examiner: Nguyen C.

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RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sirs:

In response to the Election/Restriction Requirement dated December 19, 2001, the response being due by Feb. 18, 2002 by the enclosed petition for extension of time, please enter and consider the following.

Applicant hereby elects to prosecute Group II, claims 1-4 and 15-16, drawn to methods of making a semiconductor device.

Respectfully submitted,

Alan S. Raynes

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Reg. No. 39,809

Dated: January 19, 2002

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on January 19, 2002.

Alan S. Raynes

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Jan. 19, 2002
(Date)